



RISC-V[®]

@ FOSDEM 2024

Talks are scheduled back-to-back

Crammed! Respect the schedule!

Talks are RECORDED, and LIVE BROADCAST

Enjoy! Ask questions! Be excellent!

T-shirts (speakers first!), and stickers

09:00 09:15

Welcome to the RISC-V devroom | Björn Töpel

09:15 09:45

Testing RISC-V Assembly | Nathan Egge

09:45 10:30

Lessons from porting software to RISC-V @ RISE | Ludovic Henry

10:30 11:15

MAMBO - Dynamic Binary Modification Tool for RISC-V | Igor Wodiany, John Alistair Kressel

11:15 12:00

Unleashing RISC-V in Managed Runtimes: Navigating Extensions, Memory Models, and Performance Challenges in OpenJDK | Robbin Ehn

12:00 12:30

A framework for RISC-V SBI verification and ISA extension validation | Andrew Jones

~~LLaMa.cpp: Utilizing RISC-V Vector Extension for Improving LLMs Performance | Ahmad Tameem, Umer Imran~~

<https://fosdem.org/2024/schedule/track/risc-v-devroom/>



RISC-V®