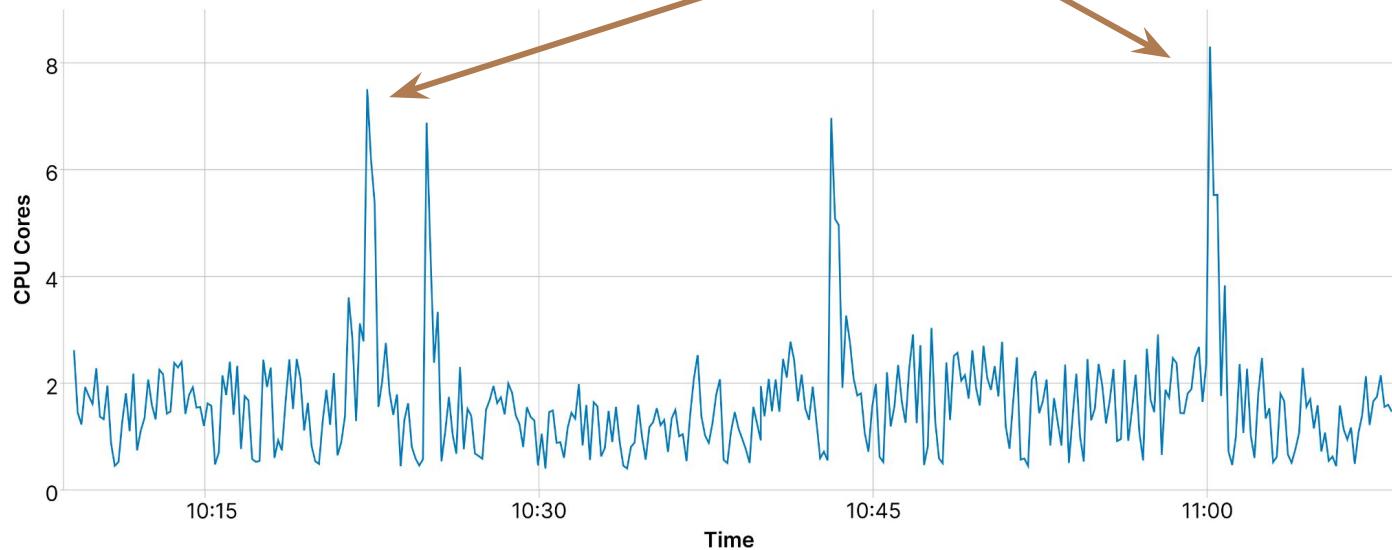


# What's possible in observability when we have frame pointers

Matthias Loibl / Jon Seager

# Continuous Profiling

What happened at the spikes?! 🤔



# Continuous Profiling



1

## runtime.goexit



# About us



Matthias Loibl



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[@metalmatze](https://github.com/metalmatze)

- Senior Software Engineer at Polar Signals
- Open-Source Maintainer
  - Parca
  - Thanos
  - Prometheus
  - Prometheus Operator
  - Pyrra



Jon Seager



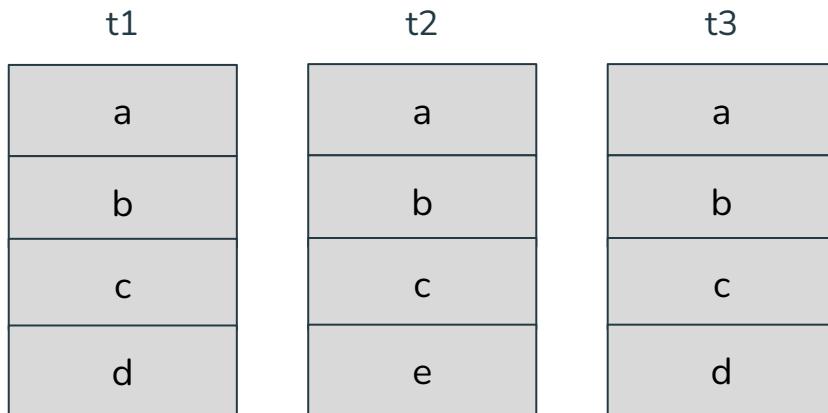
[@jnsgruk@hachyderm.io](mailto:@jnsgruk@hachyderm.io)



[@jnsgruk](https://github.com/jnsgruk)

- VP Engineering at Canonical
- Leads development of Juju & charms
  - Observability
  - Identity
  - Data Platform
  - MLOps
  - Telco

# What is profiling data made up of?

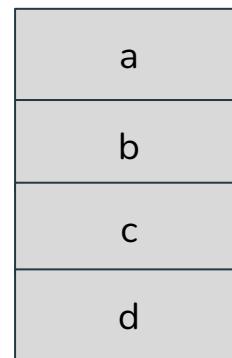


=

a;b;c;d 20ms  
a;b;c;e 10ms



# How do we get a stack?



?



# Best Case: Frame Pointers

# What are frame pointers?

```
int top(void) {  
    for(;;) { }  
}  
int c1(void) {  
    top();  
}  
int b1(void) {  
    c1();  
}  
int a1(void) {  
    b1();  
}  
int main(void) {  
    a1();  
}
```

# compiled with `gcc sample.c -o sample\_with\_frame\_pointers -fno-omit-frame-pointer`  
\$ objdump -d ./sample\_with\_frame\_pointers

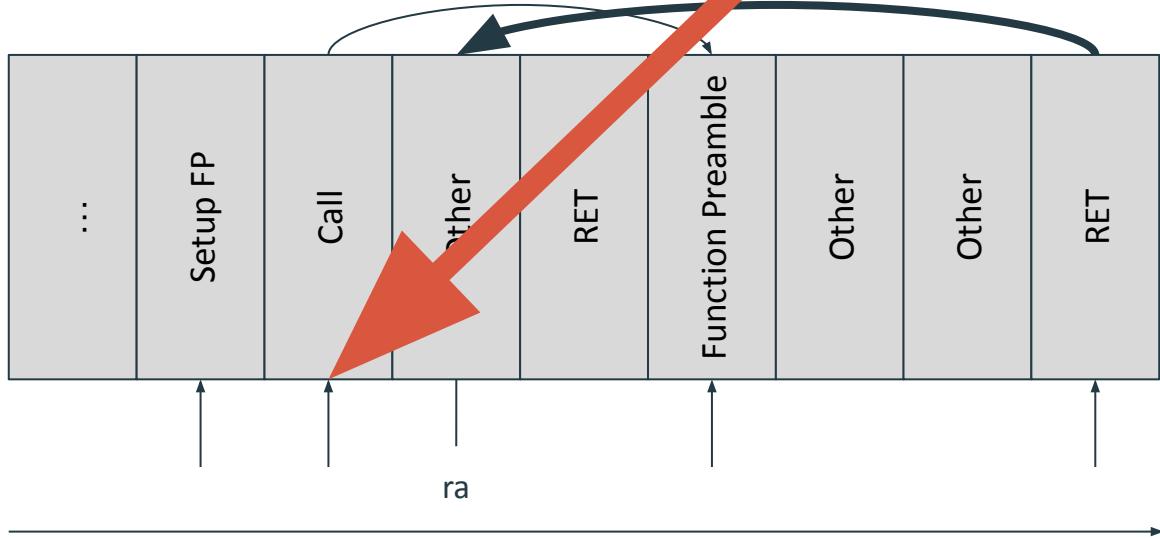
|   |  |
|---|--|
| <pre>0000000000401106 &lt;top&gt;:<br/>401106:    55<br/>401107:    48 89 e5<br/>40110a:    eb fe</pre><br><pre>000000000040110c &lt;c1&gt;:<br/>40110c:    55<br/>40110d:    48 89 e5<br/>401110:    e8 f1 ff ff ff<br/>401115:    90<br/>401116:    5d<br/>401117:    c3</pre><br><pre>0000000000401118 &lt;b1&gt;:<br/>401118:    55<br/>401119:    48 89 e5<br/>40111c:    e8 eb ff ff ff<br/>401121:    90<br/>401122:    5d<br/>401123:    c3</pre> | <pre>push %rbp<br/>mov %rsp,%rbp<br/>jmp 40110a &lt;top+0x4&gt;</pre><br><pre>push %rbp<br/>mov %rsp,%rbp<br/>call 401106 &lt;top&gt;<br/>nop<br/>pop %rbp<br/>ret</pre><br><pre>push %rbp<br/>mov %rsp,%rbp<br/>call 40110c &lt;c1&gt;<br/>nop<br/>pop %rbp<br/>ret</pre> |
|---|--|

...

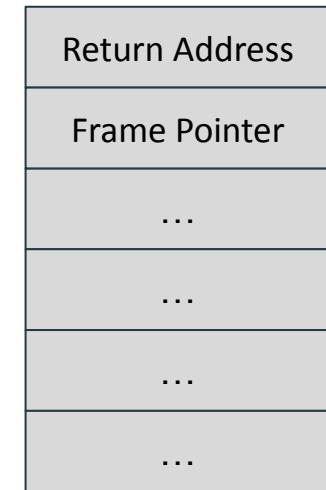


# Binary

ra - 1 is our caller

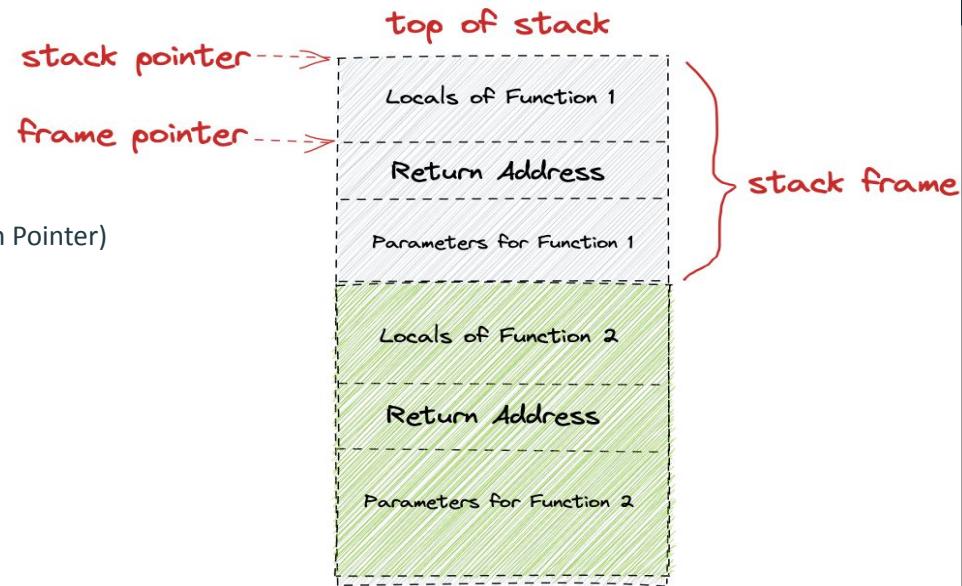


Executable Code bytes/instructions in Binary



# Walking the Stacks

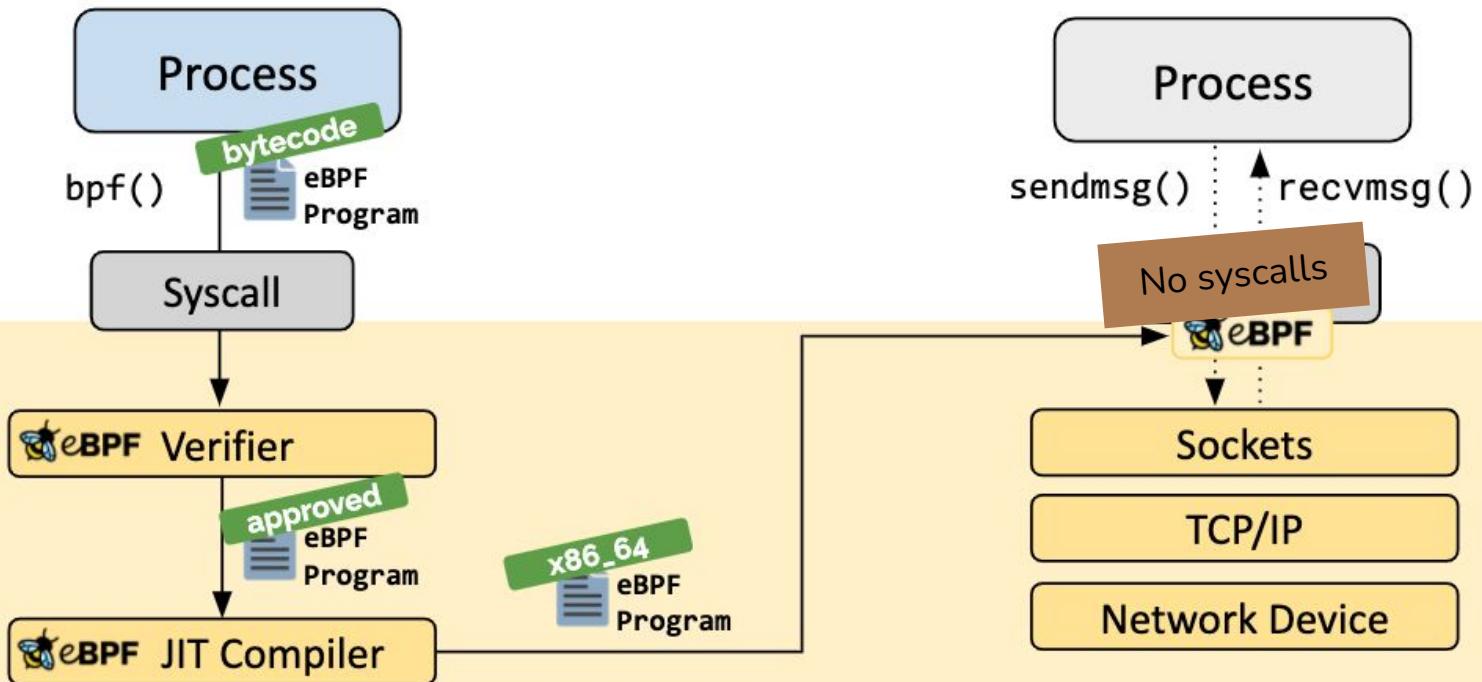
- Core registers
  - Keep track of process state
    - PC (program Counter)/ IP (Instruction Pointer)
    - rbp/fp: base pointer/frame pointer
    - rsp/sp: stack pointer
    - ra: return address



# Using frame pointers in eBPF

# Linux Kernel

## What's eBPF?



# Get a stack in BPF

- **bpf\_get\_stack**
  - BPF helper to unwind the stack using frame pointers

```
bpf_user_pt_regs_t *regs = &ctx->regs;
u64 ip = PT_REGS_IP(regs); // read leaf instruction pointer
u64 bp = PT_REGS_FP(regs); // read leaf base pointer
u64 ra = 0; // return address

// *save leaf frame*

for (int i = 0; i < MAX_STACK_DEPTH; i++) {
    // return address is the next register from rbp, so 8 bytes away
    err = bpf_probe_read_user(&ra, 8, (void *)bp + 8);
    if (err < 0) {
        // error
    }

    // Rewinding the program counter to get the instruction pointer for the
    // previous function would be ideal but is unreliable in `x86` due to
    // variable width encoding. We can ensure correctness only by disassembling
    // the `.text` section which would be unfeasible. Since return addresses
    // always point to the next instruction to be executed after returning from
    // the function (and stack grows downwards), subtracting 1 from the current
    // `ra` gives us the current instruction pointer location, if not the exact
    // instruction boundary
    ip = ra - 1;

    // *save frame*

    // read content of base pointer into bp variable
    err = bpf_probe_read_user(&bp, 8, (void *)bp);
    if (err < 0) {
        // error
    }

    // if bp == 0 we've reached the bottom of the stack
}
```



Having frame pointers in  
BPF makes regular profiling  
easy



# Why do frame pointers matter for observability?

- **Simplified Profiling:**
  - Don't worrying about compiler configurations
- **Lower Overhead:**
  - Cheaper than using DWARF or DWARF-derived information to unwind
- **Debugging Accessibility:**
  - bcc-tools, bpftrace, perf and other such tooling to work out of the box

Check out last year's FOSDEM talk!  
"Stack walking/unwinding without  
frame pointers"



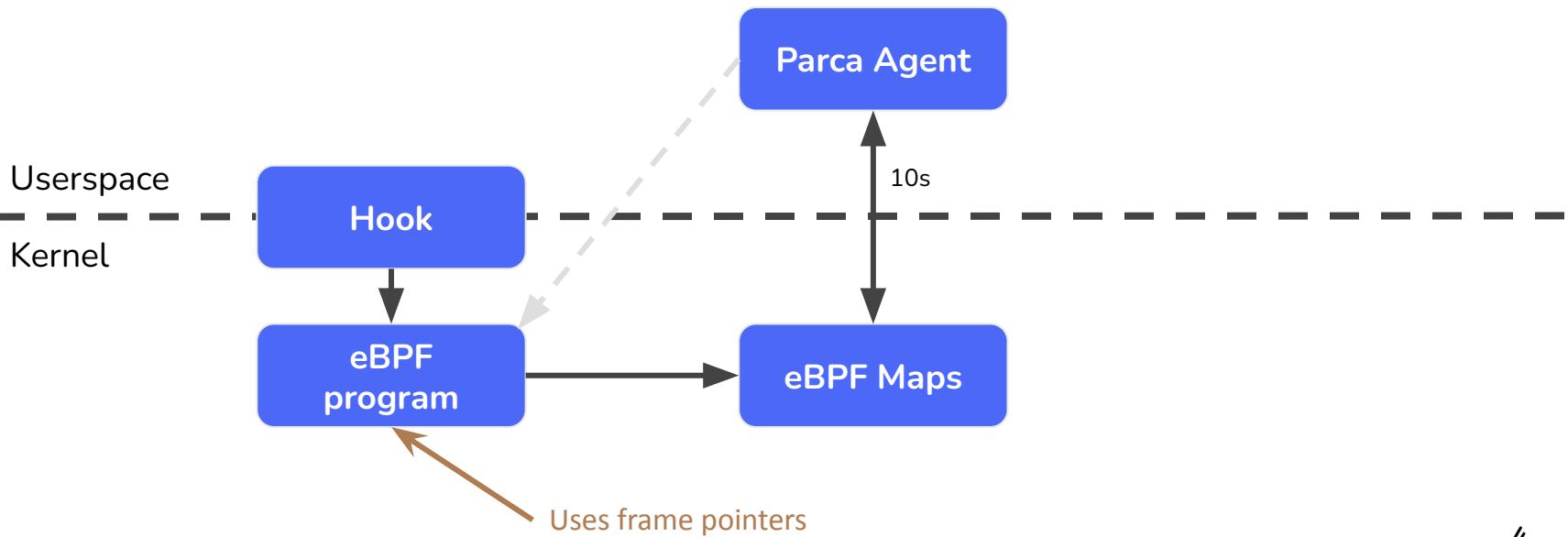
# Possibilities are endless:

Unwinding takes 2 memory reads

- bpftrace
  - [ustack builtin](#), the bpf\_get\_stack in bpftrace's language
  - bpftrace -e 'profile:hz:99 { @[ustack] = count(); }' // one-liner profiler
- [Go execution tracer](#)
- Profile Guided Optimizations (PGO) research
  - [Context-sensitive sampling-based PGO \(CSSPGO\)](#)



# Communicating with Userspace





Bringing frame pointers to  
the masses.



Ubuntu 24.04 LTS will have  
frame pointers enabled by  
default on 64-bit platforms.



Performance implications  
and future plans for  
optimisation.



Frame pointers are just the start.



Canonical is building a  
company-wide Performance  
Engineering machine.



`snap install parca`

`snap install parca-agent --classic`



`juju deploy parca[-k8s]`

# Get in touch!



Matthias Loibl



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